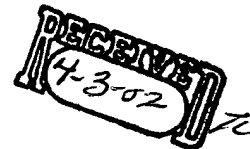


IN THE CLAIMS

Official



Please amend the following claims:

- D1
concl'd
2. (4x amended) An apparatus for removing an overflow condition comprising:
means for detecting a first digitally encoded data stream portion causing an overflow condition;
means for delaying said first data stream portion for a delay time that prevents said overflow condition; and
means for accelerating a second data stream portion that follows said first data stream portion to substantially make-up for said delay time.
- D2
concl'd
22. (2x amended) A method comprising
determining a delay caused by re-scheduling transmission of a part of new data stream data in a new data stream portion for splicing of data stream portions including an old data stream portion and the new data stream portion according to a formula:
$$(\text{currently scheduled transmission time for said portion}) + ((n \text{ packets} \times m \text{ bits/packet} \times \text{multiplexer bit rate}) / (\text{data stream bit rate}))$$

wherein n indicates a number of packets by which transmission is to be delayed, and m indicates a number of bits in a packet of data stream data to be transmitted.
- D3
concl'd
54. (2x amended) A splicer for splicing digitally encoded data streams, including an old data stream and a new data stream, comprising:
(a) means for determining, in accordance with a splice-out point of the old data stream and a splice-in point of the new data stream, a new data stream real-time transmit start time; and
(b) means for aligning the new data stream with the old data stream according to said new data stream real-time transmit time, said means for aligning both delaying and accelerating said new data stream when splicing said old data stream and said new data stream such that certain data portions are accelerated to make up for previously introduced delay of other data portions.

- D4
concl.
79. (2x) A digitally encoded data stream transmitter comprising:
shifting means for determining an amount by which scheduled transmission times of data stream portions are to be accelerated and delayed, such that certain data portions are accelerated to make up for previously introduced delay of other data portions; and
transmitting means for transmitting said data stream portions at transmission times accelerated and delayed by the amount determined by said shifting means.

Please add the following new claims:

6/4
106. The method according to claim 1 wherein the steps of delaying and accelerating operate upon the first data stream portion and the second data stream portion, respectively, that each contain a plurality of program clock references.

107. The method according to claim 106 wherein the steps of delaying and accelerating operate upon the first data stream portion and the second data stream portion, respectively, that each further contain a plurality of digital video frames.

D5
108. The method according to claim 107 wherein at least some of the digital video frames in the first data stream portion and the second data stream portion further include a decode time stamp field.

109. The method according to claim 108 wherein at least some of the video frames in each of the first data stream portion and the second data stream portion further include a presentation time stamp field.

110. The method according to claim 108 wherein the step of detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

111. The method according to claim 106 wherein the step of detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

112. The method according to claim 1 wherein the first data stream portion and the second data stream portion are digital video streams.

113. The method according to claim 112 wherein the digital video streams are MPEG-2 video streams.

114. The method according to claim 1 wherein the step of detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

115. The method according to claim 1 wherein the steps of delaying and accelerating operate upon the first data stream portion and the second data stream portion, respectively, that each contain a plurality of video frames.

D5
Cont.
G/B
116. The method according to claim 115 wherein at least some of the video frames in each of the first data stream portion and the second data stream portion each contain a decode time stamp field.

117. The method according to claim 116 wherein at least some of the video frames in each of the first data stream portion and the second data stream portion each contain a presentation time stamp field.

118. The method according to claim 115 wherein the step of detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

S/B
119. The method according to claim 87 wherein the steps of delaying and accelerating operate upon the first data stream portion and the second data stream portion, respectively, that each contain a plurality of program clock references.

120. The method according to claim 119 wherein the steps of delaying and accelerating operate upon the first data stream portion and the second data stream portion, respectively, that each further contain a plurality of digital video frames.

121. The method according to claim 120 wherein at least some of the digital video frames in the first data stream portion and the second data stream portion further include a decode time stamp field.

122. The method according to claim 121 wherein at least some of the video frames in each of the first data stream portion and the second data stream portion further include a presentation time stamp field.

123. The method according to claim 121 wherein the step of detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

DS
SM
124. The apparatus according to claim 2 wherein the means for delaying and accelerating operate upon the first data stream portion and the second data stream portion, respectively, that each contain a plurality of program clock references.

125. The apparatus according to claim 124 wherein the means for delaying and accelerating operate upon the first data stream portion and the second data stream portion, respectively, that each further contain a plurality of digital video frames.

126. The apparatus according to claim 125 wherein at least some of the digital video frames in the first data stream portion and the second data stream portion further include a decode time stamp field.

127. The apparatus according to claim 126 wherein at least some of the video frames in each of the first data stream portion and the second data stream portion further include a presentation time stamp field.

128. The apparatus according to claim 126 wherein the means for detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

129. The apparatus according to claim 2 wherein the first data stream portion and the second data stream portion are digital video streams.

130. The apparatus according to claim 129 wherein the digital video streams are MPEG-2 video streams.

131. The apparatus according to claim 2 wherein the means for detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

132. The apparatus according to claim 2 wherein the means for delaying and accelerating operate upon the first data stream portion and the second data stream portion, respectively, that each contain a plurality of video frames.

DS
S18
133. The apparatus according to claim 132 wherein at least some of the video frames in each of the first data stream portion and the second data stream portion each contain a decode time stamp field.

134. The apparatus according to claim 133 wherein at least some of the video frames in each of the first data stream portion and the second data stream portion each contain a presentation time stamp field.

135. The apparatus according to claim 132 wherein the means for detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

S19
136. The method according to claim 20 wherein the steps of causing the delay and the acceleration operate upon the old data stream portion and the new data stream portion, respectively, that each contain a plurality of program clock references.

137. The method according to claim 136 wherein the steps of causing the delay and acceleration operate upon the old data stream portion and the new data stream portion, respectively, that each further contain a plurality of digital video frames.

138. The method according to claim 137 wherein at least some of the digital video frames in the old data stream portion and the new data stream portion further include a decode time stamp field.

139. The method according to claim 138 wherein at least some of the video frames in each of the old data stream portion and the new data stream portion further include a presentation time stamp field.

140. The method according to claim 138 wherein the step of detecting the first digitally encoded data stream portion detects a contiguous but not continuous series of packets.

141. The method according to claim 20 wherein the old data stream portion and the new data stream portion are digital video streams.

142. The method according to claim 141 wherein the digital video streams are MPEG-2 video streams.

143. The method according to claim 20 wherein the steps of causing the delay and the acceleration operate upon the old data stream portion and the new data stream portion, respectively, that each contain a plurality of video frames.

144. The method according to claim 143 wherein at least some of the video frames in each of the old data stream portion and the new data stream portion each contain a decode time stamp field.

145. The method according to claim 144 wherein at least some of the video frames in each of the old data stream portion and the new data stream portion each contain a presentation time stamp field.

146. The method according to claim 91 wherein the steps of causing the delay and the acceleration operate upon the old data stream portion and the new data stream portion, respectively, that each contain a plurality of program clock references.

147. The method according to claim 146 wherein the steps of causing the delay and the acceleration operate upon the old data stream portion and the new data stream portion, respectively, that each further contain a plurality of digital video frames.

148. The method according to claim 147 wherein at least some of the digital video frames in the old data stream portion and the new data stream portion further include a decode time stamp field.

DS added

149. The method according to claim 148 wherein at least some of the video frames in each of the old data stream portion and the new data stream portion further include a presentation time stamp field.—
